

# **CM17202**

## **PC/104-*Plus* Fast Ethernet Controller utilityModule™**

### **User's Manual**



BDM-610020025  
Rev. A



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**CM17202**  
**PC/104-*Plus* Fast Ethernet Controller**  
**utilityModule™**  
**User's Manual**

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Rev. A	New Manual Naming Method	

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## Chapter 1 INTRODUCTION

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- This manual gives information on the CM17202 PC/104-*Plus* Fast Ethernet Controller utilityModule™.

This module provides a twisted-pair 100 Mbit ethernet connection to support the Real Time Devices' PC/104-*Plus* cpuModules™ and other PC/104-*Plus* processor modules.

### Features

The following are major features of the CM17202 utilityModule™.

Intel 21143-TD PCI Fast Ethernet Controller and ICS1892Y 10/100BaseTX integrated physical transceiver

- MII Interface

## ***Electrical Specifications***

- Compatible with PCI Specification
- Compatible with PC/104-*Plus* Specification

## ***Mechanical Specifications***

- Compatible with PC/104-*Plus* Specification when using the 10 pin twisted pair connector, CN5. This connector is not the default factory installed connector. The factory default connector is the RJ45 connector, CN4. Please note that all RJ45 connectors (default configuration), violate the height limit of all PC/104 Specifications. If height limit is required, the RJ45 connector cannot be installed, and the optional 10 pin connector must be installed by the factory.
- Dimensions: 3.8 x 3.9 x 0.6" (97 x 100 x 16 mm)
- Weight (mass): 3.0 ounces (85 grams)



## ***Environmental***

- Operating Temperature: -40° to 85° C
- Storage Temperature: -40° to +85° C
- Non-Condensing Relative Humidity: < 95% at 40°C

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<b>WARNING!</b>	Like all equipment using CMOS devices, the CM17202 must be protected from static discharge. Never touch any of the parts except at static-free workstation. Use anti-static bag shipped with the CM17202 to handle the board
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## ***Software***

The 21143-TD is supported by DOS, Windows 3.1, Windows 95, Windows NT, Windows 2000, VxWorks and QNX. For DOS and Windows 3.1, drivers can be downloaded from the Intel Developer web site at the 21143 Drivers page. For Windows 95, Windows 98, Windows NT, and Windows 2000, the drivers are supplied by Microsoft along with the operating system. The latest drivers for all of the above mentioned operating systems can also be found at the Intel Developer web site at the 21143 Drivers page. For VxWorks and QNX please contact them directly.

## Chapter 2 CM17202 LAYOUT

### Finding Pin 1 of Connectors

The three possible ways to determine pin 1 on the connectors are:

1. A square white area silk-screened on the PC board
2. A square solder pad visible on the bottom of the PC board
3. A numeral 1 silk-screened near pin 1 on the top of the PC board

### Component Locations

The figure below shows switch and jumper locations.

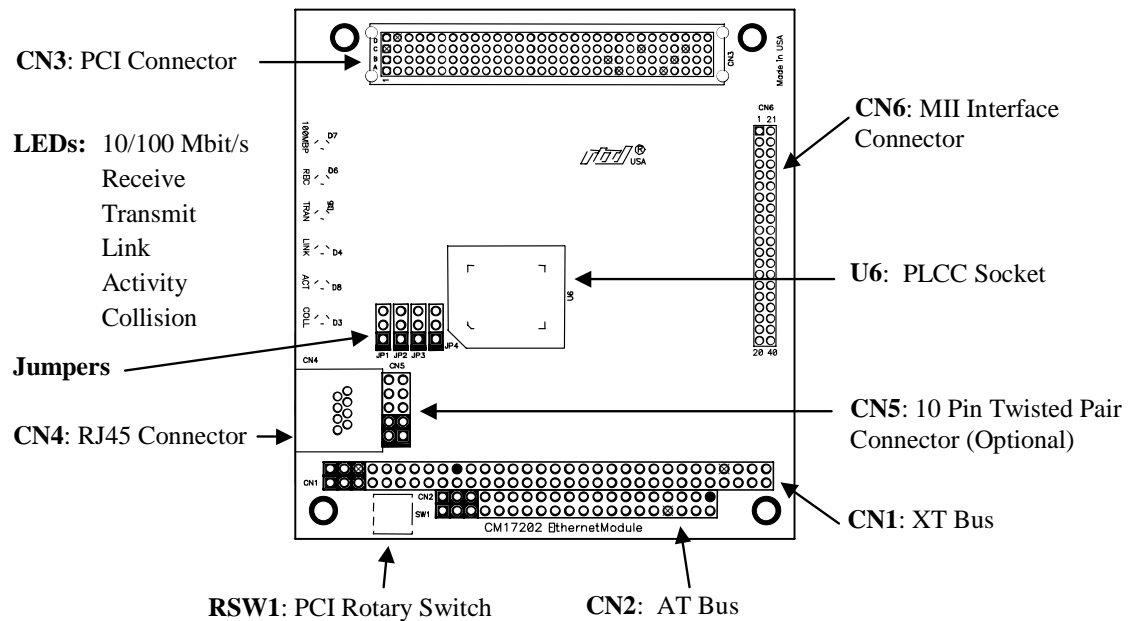


Figure 1: Component Locations

## ***List of Connectors and Switches***

<b>Connector</b>	<b>Function</b>	<b>Dimension</b>
CN1, CN2	XT/AT Bus Connector	64 + 32 pins
CN3	PCI Bus Connector	120 pins
CN4	Twisted Pair Ethernet Connector	RJ45
CN5	Twisted Pair Ethernet Connector	10 pins
CN6	MII Interface Connector	40 pins
SW1:	PCI Board Selector Rotary Switch	Rotary Switch

**Table 1:** List of Connectors and Switches

## PC/104 Bus Connectors, CN1 and CN2

Connectors CN1 and CN2 provide PC/104 bus connections. CN1 carries XT bus signals, and CN2 carries additional signals for the AT bus.

PC/104 XT Bus Connector, CN1		
Pin	Row A	Row B
1	IOCHCHK*	0V
2	SD7	RESETDRV
3	SD6	+5V
4	SD5	IRQ9
5	SD4	-5V
6	SD3	DRQ2
7	SD2	-12V
8	SD1	ENDXFR*
9	SD0	+12V
10	IOCHRDY	(KEYING PIN)
11	AEN	SMEMW*
12	SA19	SMEMR*
13	SA18	IOW*
14	SA17	IOR*
15	SA16	DACK3
16	SA15	DRQ3
17	SA14	DACK1*
18	SA13	DRQ1
19	SA12	REFRESH
20	SA11	SYSCLK
21	SA10	IRQ7
22	SA9	IRQ6
23	SA8	IRQ5
24	SA7	IRQ4
25	SA6	IRQ3
26	SA5	DACK2*
27	SA4	TC
28	SA3	BALE
29	SA2	+5V
30	SA1	OSC
31	SA0	0V
32	0V	0V

**Table 2:** PC/104 XT Bus Connector

PC/104 AT Bus Connector, CN2		
Pin	Row C	Row D
0	0V	0V
1	SBHE*	MEMCS16*
2	LA23	IOCS16*
3	LA22	IRQ10
4	LA21	IRQ11
5	LA20	IRQ12
6	LA19	IRQ15
7	LA18	IRQ14
8	LA17	DACK0*
9	MEMR*	DRQ0
10	MEMW*	DACK5*
11	SD8	DRQ5
12	SD9	DACK6*
13	SD10	DRQ6
14	SD11	DRQ6
15	SD12	DRQ7
16	SD13	+5V
17	SD14	MASTER*
18	SD15	0V
19	(KEYING PIN)	0V

**Table 3:** PC/104 AT Bus Connector

---

**Note:** Two locations on the bus have mechanical keying pins to help prevent misconnection of the PC/104 bus. These keying pins are a part of the PC/104 standard, and we strongly recommend you leave them in place.

If you have other modules without keying pins, we suggest you modify them to include keying.

---

## PC/104-Plus PCI connector, CN3

Connector CN3 carries the signals of the PC/104-Plus PCI bus. These signals match definitions of the PCI Local Bus specification Revision 2.1. The following tables list the pinouts of the PC/104-Plus bus connector.

Pin	A	B	C	D
1	GND/5.0V KEY <sup>1</sup>	Reserved	+5V	AD00
2	VI/O	AD02	AD01	+5V
3	AD05	GND	AD04	AD03
4	C/BE0*	AD07	GND	AD06
5	GND	AD09	AD08	GND
6	AD11	VI/O	AD10	M66EN
7	AD14	AD13	GND	AD12
8	+3.3V	C/BE1*	AD15	+3.3V
9	SERR*	GND	SB0*	PAR
10	GND	PERR*	+3.3V	SDONE
11	STOP*	+3.3V	LOCK*	GND
12	+3.3V	TRDY*	GND	DEVSEL*
13	FRAME*	GND	IRDY*	+3.3V
14	GND	AD16	+3.3V	C/BE2*
15	AD18	+3.3V	AD17	GND
16	AD21	AD20	GND	AD19
17	+3.3V	AD23	AD22	+3.3V
18	IDSEL0	GND	IDSEL1	IDSEL2
19	AD24	C/BE3*	VI/O	IDSEL3
20	GND	AD26	AD25	GND
21	AD29	+5V	AD28	AD27
22	+5V	AD30	GND	AD31
23	REQ0*	GND	REQ1*	VI/O
24	GND	REQ2*	+5V	GNT0*
25	GNT1*	VI/O	GNT2*	GND
26	+5V	CLK0	GND	CLK1
27	CLK2	+5V	CLK3	GND
28	GND	INTD*	+5V	RST*
29	+12V	INTA*	INTB*	INTC*
30	-12V	Reserved	Reserved	GND/3.3V KEY <sup>1</sup>

**Table 4:** PC/104-Plus Bus Signal Assignments

**Note:** The KEY pins are to guarantee proper module installation. Pin-A1 will be removed and the female side plugged for 5.0V I/O signals and Pin-D30 will be modified in the same manner for 3.3V I/O. It is recommended that both KEY pins (A1 and D30) be electrically connected to GND for shielding.

## **PC/104-Plus PCI Bus Signals**

The following table contains brief descriptions of the PC/104-Plus PCI bus signals.

### **Address and Data**

**AD[31:00]** -- Address and Data are multiplexed. A bus transaction consists of an address cycle followed by one or more data cycles.

**C/BE[3:0]\*** -- Bus Command/Byte Enables are multiplexed. During the address cycle, the command is defined. During the Data cycle, they define the byte enables.

**PAR** -- Parity is even on AD[31:00] and C/BE[3:0]\* and is required.

### **Interface Control Pins**

**FRAME\*** -- Frame is driven by the current master to indicate the start of a transaction and will remain active until the final data cycle.

**TRDY\*** -- Target Ready indicates the selected device's ability to complete the current data cycle of the transaction. Both IRDY\* and TRDY\* must be asserted to terminate a data cycle.

**IRDY\*** -- Initiator Ready indicates the master's ability to complete the current data cycle of the transaction.

**STOP\*** -- Stop indicates the current selected device is requesting the master to stop the current transaction.

**DEVSEL\*** -- Device Select is driven by the target device when its address is decoded.

**IDSEL** -- Initialization Device Select is used as a chip-select during configuration.

**LOCK\*** -- Lock indicates an operation that may require multiple transactions to complete.

### **Error Reporting**

**PERR\*** -- Parity Error is for reporting data parity errors.

**SERR\*** -- System Error is for reporting address parity errors.

### **Arbitration (Bus Masters Only)**

**REQ\*** -- Request indicates to the arbitrator that this device desires use of the bus.

**GNT\*** -- Grant indicates to the requesting device that access has been granted.

### **System**

**CLK** -- Clock provides timing for all transactions on the PCI bus.

**RST\*** -- Reset is used to bring PCI-specific registers to a known state.

### **Interrupts**

**INTA\*** -- Interrupt A is used to request Interrupts.

**INTB\*** -- Interrupt B is used to request Interrupts only for multi-function devices.

**INTC\*** -- Interrupt C is used to request Interrupts only for multi-function devices.

**INTD\*** -- Interrupt D is used to request Interrupts only for multi-function devices.

### **Power Supplies and VIO**

**+5V** -- +5 volt supply

**+12V** -- +12 volt supply

**-12V** -- -12 volt supply

**+3.3V** -- +3.3 volt supply

**VIO** -- This signal typically is the I/O power to the bus drivers on a PCI bus card.



## ***RJ45 Twisted Pair Ethernet, CN4***

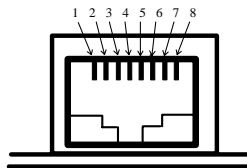
Connector CN4 is for UTP (Unshielded Twisted Pair) wiring normally used for 10/100Base-T Ethernet. It is the default factory installed twisted pair connector on the board.

The following table gives the pinout of CN4.

Pin	Signal	Function	in/out
1	TX+	Transmit +	out
2	TX-	Transmit -	out
3	RX+	Receive +	in
4	N.C.	Reserved	
5	N.C.	Not connected	
6	RX-	Receive -	in
7	N.C.	Not connected	
8	N.C.	Not connected	

**Table 5:** RJ45 Signal Assignments

The figure below shows the pin numbering of CN4 when **looking into the connector**:



**Figure 2:** RJ-45 Jack Connector

CN4 is a standard female RJ-45 connector.

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**Note:** The RJ45 connector exceeds the height limit of the PC/104 specifications. The optional CN5 connector is also a 10/100Base-TX connection and can be factory installed if the RJ45 connector height is a problem

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## ***Twisted Pair Ethernet, CN5***

This is an optional connector which can be factory installed in place of the RJ45 connector. This connector is not installed when the RJ45 connector, CN4, is installed

<b>Pin</b>	<b>Signal</b>	<b>Function</b>	<b>in/out</b>
1	TX+	Transmit +	out
2	RX-	Receive -	in
3	TX-	Transmit -	out
4	N.C.	Not connected	
5	RX+	Receive +	in
6	N.C.	Not connected	
7	N.C.	Reserved	
8	N.C.	Not connected	
9	N.C.	Not connected	
10	N.C.	Not connected	

**Table 6:** CN5 Signal Assignments

## MII Connector, CN6

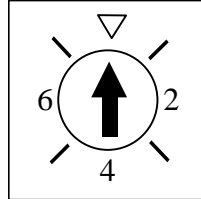
Pin Number	I/O	Type	Name	Description
1, 20, 21, 40		Power	+5V	5 Volt supply
22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39		Power	GND	Ground
2	I/O	TTL	MDIO	Management Data Input Output
3	I	TTL	MDC	Management Data Clock
4	O	TTL	RXD3	Receive Data 3
5	O	TTL	RXD2	Receive Data 2
6	O	TTL	RXD1	Receive Data 1
7	O	TTL	RXD0	Receive Data 0
8	O	TTL	RXDV	Receive Data Valid
9	O	TTL	RXCLK	Receive Clock
10	O	TTL	RXER	Receive Error
11	-----	Pulldown	-----	-----
12	O	TTL	TXCLK	Transmit Clock
13	I	TTL	TXEN	Transmit Enable
14	I	TTL	TXD0	Transmit Data 0
15	I	TTL	TXD1	Transmit Data 1
16	I	TTL	TXD2	Transmit Data 2
17	I	TTL	TXD3	Transmit Data 3
18	O	TTL	COL	Collision Detect
19	O	TTL	CRS	Carrier Sense

Table 7: MII Interface Connector Layout

## PCI Board Selector Rotary Switch, SW1

Since the utilityModule™ uses stack through buses, the only hardware installation you will need to do is to place the module onto the PC/104-*Plus* or PCI-104 stack. To do this, you will connect the PCI and/or ISA bus connectors on the CM17202 to the respective connectors of your stack.

Before you can use this module you have to set the PCI board selector rotary switch located on the module (SW1). If this module is the first module from the CPU module select '0,' if it is the second module select '1,' etc.



**Figure 3:** PCI Selector Rotary Switch

## LED Indicators

The LEDs on the board are used to indicate various operations of the on-board physical layer chip. These LEDs do not indicate any operations that may be occurring on an add-on physical layer board which is connected through the MII connector. Listed below are the LEDs starting with the one closest to the PC/104 connector.

Diode #	DESCRIPTION	Color
1	Collision	RED
2	Activity	YELLOW
3	Link	GREEN
4	Transmit	YELLOW
5	Receive	GREEN
6	10/100	YELLOW

**Table 8:** LED Indicators

## Chapter 3 **INSTALLING THE UTILITYMODULE™**

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**WARNING!** Like all equipment using CMOS devices, the CM17202 must be protected from static discharge. Never touch any of the parts except at static-free workstation. Use anti-static bag shipped with the CM17202 to handle the board

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The following sections contain information on configuring the CM17202 PC/104-*Plus* Fast Ethernet Controller utilityModule™. **Please read the entire section** before attempting to use the utilityModule™!

### ***Recommended procedure:***

We recommend you follow the procedure below to ensure the stacking of the modules does not damage connectors or electronics.

- 0 Turn off power to the PC/104-*Plus* system or stack.
- 1 Select and install standoffs to properly position the utilityModule™ on the PC/104-*Plus* stack.
- 2 Touch a grounded metal part of the stack to discharge any buildup of static electricity.
- 3 Remove the utilityModule™ from its anti-static bag.
- 4 Check to see if the keying pins in the PCI bus connector are properly positioned.
- 5 Check the stacking order and set the PCI Board Selector Rotary Switch, RSW1 (see Section 2.13).
- 6 Hold the utilityModule™ by its edges and orient it so that the bus connector pins line up with the matching connector pins on the stack.
- 7 Gently and evenly press the utilityModule™ onto the PC/104-*Plus* stack.

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**CAUTION:** Do not force the module onto the stack! Wiggling the module or applying too much force may damage it. If the module does not readily press into place, remove it, check for bent pins or out-of-place keying pins, and try again.

---

## Jumper Settings

You cannot use both the RJ45 connector and the MII connector at the same time. Table 10 lists the Jumper settings to select each of the connectors.

Jumper	Jumper Position	
	RJ45 Connector Selected	MI I Connector Selected
JP1	1-2	2-3
JP2	1-2	1-2
JP3	1-2	2-3
JP4	1-2	2-3

**Table 9:** Connector Jumper Settings

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**Note:** In order to use the MII Connector for an external transceiver, the user must set Jumpers JP1, JP3, and JP4 to the 2-3 position and JP2 to the 1-2 position. Likewise, the user must set Jumpers JP1, JP2, JP3, and JP4 to the 1-2 position to use the RJ45 Connector.

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## Chapter 4 RETURN POLICY AND WARRANTY

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### ***Return Policy***

If you wish to return a product to the factory for service, please follow this procedure:

Read the Limited Warranty to familiarize yourself with our warranty policy.

Contact the factory for a Return Merchandise Authorization (RMA) number.

Please have the following available:

- Complete board name
- Board serial number
- A detailed description of the board's behavior

**List the name of a contact person**, familiar with technical details of the problem or situation, **along with their phone and fax numbers, address, and e-mail address** (if available).

**List your shipping address!!**

Indicate the shipping method you would like used to return the product to you.

*We will not ship by next-day service without your pre-approval.*

*Carefully package the product, using proper anti-static packaging.*

*Write the RMA number in large (1") letters on the outside of the package.*

*Return the package to:*

*RTD Embedded Technologies, Inc.*

*103 Innovation Blvd.*

*State College PA 16803-0906*

*USA*

## ***Limited Warranty***

RTD Embedded Technologies, Inc. warrants the hardware and software products it manufactures and produces to be free from defects in materials and workmanship for one year following the date of shipment from RTD Embedded Technologies, INC. This warranty is limited to the original purchaser of product and is not transferable.

During the one year warranty period, RTD Embedded Technologies will repair or replace, at its option, any defective products or parts at no additional charge, provided that the product is returned, shipping prepaid, to RTD Embedded Technologies. All replaced parts and products become the property of RTD Embedded Technologies. Before returning any product for repair, customers are required to contact the factory for an RMA number.

THIS LIMITED WARRANTY DOES NOT EXTEND TO ANY PRODUCTS WHICH HAVE BEEN DAMAGED AS A RESULT OF ACCIDENT, MISUSE, ABUSE (such as: use of incorrect input voltages, improper or insufficient ventilation, failure to follow the operating instructions that are provided by RTD Embedded Technologies, "acts of God" or other contingencies beyond the control of RTD Embedded Technologies), OR AS A RESULT OF SERVICE OR MODIFICATION BY ANYONE OTHER THAN RTD Embedded Technologies. EXCEPT AS EXPRESSLY SET FORTH ABOVE, NO OTHER WARRANTIES ARE EXPRESSED OR IMPLIED, INCLUDING, BUT NOT LIMITED TO, ANY IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE, AND RTD Embedded Technologies EXPRESSLY DISCLAIMS ALL WARRANTIES NOT STATED HEREIN. ALL IMPLIED WARRANTIES, INCLUDING IMPLIED WARRANTIES FOR MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE, ARE LIMITED TO THE DURATION OF THIS WARRANTY. IN THE EVENT THE PRODUCT IS NOT FREE FROM DEFECTS AS WARRANTED ABOVE, THE PURCHASER'S SOLE REMEDY SHALL BE REPAIR OR REPLACEMENT AS PROVIDED ABOVE. UNDER NO CIRCUMSTANCES WILL RTD Embedded Technologies BE LIABLE TO THE PURCHASER OR ANY USER FOR ANY DAMAGES, INCLUDING ANY INCIDENTAL OR CONSEQUENTIAL DAMAGES, EXPENSES, LOST PROFITS, LOST SAVINGS, OR OTHER DAMAGES ARISING OUT OF THE USE OR INABILITY TO USE THE PRODUCT.

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